

Subclass	Class	ISSUE CLASSIFICATION

FILED UNDER 35 U.S.C. 36

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

MR
SCANNED 103 OA 003

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/856982	D F	356	237.4	2877 2879	Nguyen Si
APPLICANTS Yoshihiko Yamada Osamu Nakamura Takeshi Kikuchi Noboru Kudo					
TITLE Method for preparing a type of semiconductor wafer					

PTO-2040
12/99

ISSUING CANCELLATION

TERMINAL DISCLAIMER	DRAWINGS			TOTAL NUMBER OF DRAWINGS FOR O.G.
	Sheets Drawn	Figs. Drawn	Print Fig.	
<input type="checkbox"/> The term of this patent Subsequent to _____ (date) has been disclaimed. <input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	(Assistant Examiner) _____ (Date) (Primary Examiner) _____ (Date)			
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date)			

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

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